JUN 1 5 2005

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

Attorney Docket No. ___ TI-30553

In re application of: CERTIFICATION OF FAX TRANSMITTAL Joseph R. Zbiciak I hereby certify that the above correspondence is being facsimile transmitted to the Patent and Trademark Office on June 15, 2005. Serial No.: 09/703,034 Microprocessor With Rounding Dot Product Instruction Filed: October 31, 2000 Conf. No.: 8913 Group Art Unit: X To: Commissioner for Patents Sir: Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision in the Office Action dated February 25, 2005 and the Advisory Action dated June 6, 2005 of the Primary Examiner finally rejecting Claims 1, 4, 5, 9-11, 13, 16 and 17. The item(s) checked below are appropriate: An extension of time to respond to the final rejection was granted on \mathbf{X} is requested for (1) month - see attached copy. A timely response to the final rejection has been filed, as provided in 841 O.G. 1411. 2. 3. \mathbf{X} Fee \$500.00: Not required (Fee paid in prior appeal) The Commissioner is hereby authorized to charge any fees which may be required, or credit \mathbf{X}_{-} any overpayment to Texas Instruments Incorporated, Deposit Account No. 20-0668. 06/16/2005 HLE333 00000003 200668 09703034 02 FC:1401 500.00 DA Signature [Rule 191(b)] Robert D. Marshall, Jr., Reg. No. 28,527 Post Office Address Texas Instruments Incorporated P.O. Box 655474, MS 3999

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(to which correspondence is to be sent)